

# REFERENCE DESIGN

July 10, 2025

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## Reference Design:

Perfecting PCIe with AUBoard-15P Development Kit

## Introduction:

One of the commonly used interfaces to connect systems with a FPGA is PCIe. PCIe enables high bandwidth transfers and is commonly used in our laptop and desktop computers. It is also used significantly in a range of embedded computing applications which is where our FPGA comes into play.

This reference design will guide you through an example design implementation of PCIe on the Artix UltraScale+ based AUBoard-15P Development Kit

## Webpage:

[Perfecting PCIe with AUBoard - Hackster.io](http://hackster.io/perfecting-pcie-with-auboard-15p-development-kit)

## Additional Support:

For additional support, please review the discussions and post your questions in the AUBoard-15P Development Kit Forum located here: <http://avnet.me/AUBoard-15P-forum>

Alternatively, you can also reach out to your local Avnet Field Application Engineer (FAE) for support.

## Revision History:

Date	Version	Revision
10-July-25	1.0	Initial Release