

Dear Avnet Customer,

Thank you for selecting the Artix®-7 50T FPGA Evaluation Kit. Based on the Xilinx® Artix-7 50T FPGA, this kit contains a small form factor development system complete with tools and IP needed for developing Artix-7 based industrial networking, MicroBlaze™ soft processor embedded systems, and general purpose prototyping applications. Multiple reference designs are provided, making an easy starting point for development efforts.

#### **What's Inside the Box**

- Avnet 7A50T Evaluation Board
  - Artix-7 FPGA XC7A50T-FTG256-1
  - 256 MB DDR3 SDRAM
  - 32 MB Quad-SPI Flash
  - 32 KB I2C EEPROM
  - Dual 10/100 Ethernet interfaces that support IEEE1588
  - 6 Digilent compatible Pmod™ interfaces
  - 512b EEPROM with SHA-256 authentication engine
  - USB UART
  - 16 AMS inputs
  - Onboard USB-JTAG Programming
- Micro USB cables (2)
- Ethernet cable
- Xilinx Vivado® Design Edition (device locked to XC7A50T)
- Getting Started with the Artix-7 50T FPGA Evaluation Kit
- Online documentation ([www.em.avnet.com/artix7evl](http://www.em.avnet.com/artix7evl))
  - Board errata
  - Schematics
  - BOM
  - Hardware User Guide
  - Targeted reference designs

Refer to the Quick Start Card for assistance in setting up your development system and to obtain the appropriate tools and demo files. Please visit the product page for more details and updated information: [www.em.avnet.com/artix7evl](http://www.em.avnet.com/artix7evl)

Sincerely,

*Avnet Technical Marketing*