

Xilinx® Artix®-7 50T FPGA Evaluation Kit



Electronics Marketing



Technology

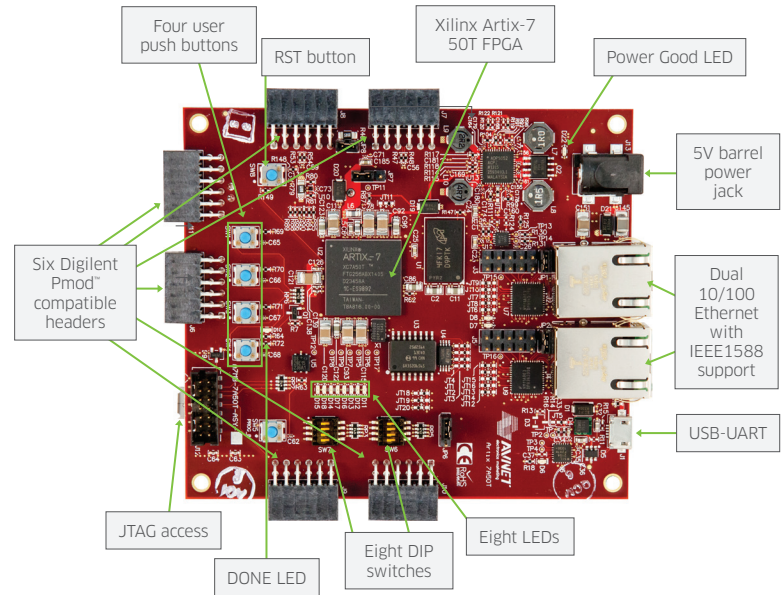
Quick Start

1. If not previously done, use the instructions available from the website listed below to download and install the Silicon Labs CP2104 USB-to-UART driver.
www.silabs.com/products/mcu/Pages/USBtoUARTBridgeVCPDrivers.aspx
2. Connect the UART port of the 7A50T board (J1) to a PC using the supplied MicroUSB cable.
3. Board will power on. Green Power Good LED (D22) should illuminate.
4. Wait a few seconds. The blue Done LED (D10) should illuminate.
5. Use Device Manager to determine the COM Port. Open a Terminal Program, configure to 115200 baud, 8 bits, no parity, 1 stop bit.
6. Reset the processor by pressing and releasing the RST button.
7. In the Terminal Window, a software application menu should boot prompting selection of peripheral tests to run and demonstrate the basic capabilities of the 7A50T board.
8. When you are done simply disconnect the USB cable from the 7A50T board.

Features

USB-UART: USB port provides UART connection to host PC and is primary power source for the 7A50T board.

SWITCHES and LEDs: Eight LEDs, four push-button switches, and eight DIP switches are accessible through a GPIO peripheral.



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