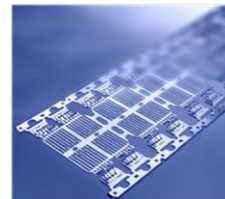


# Automotive Power MOSFET

Customer Presentation

January 2017

[www.infineon.com/automotivemosfet](http://www.infineon.com/automotivemosfet)



# Automotive MOSFET

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# Automotive MOSFET

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# Infineon and International Rectifier: A Powerful Combination



+ IOR



= A Powerful  
Combination



**Together**  
we have an even  
**larger and more diverse**  
portfolio of  
**Automotive MOSFET**  
**products**  
to offer you!

# Automotive MOSFETs (Combined IR + IFX) Selection Guide



[www.infineon.com/automotivemosfet](http://www.infineon.com/automotivemosfet)



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> Products > Power > Power MOSFET > 20V-600V Auto

## Automotive MOSFET - Product Overview

Products
Highlights
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Software
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Support

### 20V-600V

#### Infineon Op

We continuously offer  
MOSFET technology,

- Leading R<sub>DS(on)</sub>
- Highest current
- Lowest switch
- Robust packa

20V-600V Autom

> 20V-40V N-Channel

> 100V-300V N-Chann

> 20V-150V P-Chann



Adobe Acrobat  
Document

**Automotive MOSFETs**  
Product Overview

**Infineon**  
+ **IR**  
= **A Powerful Combination**

[www.infineon.com/automotivemosfet](http://www.infineon.com/automotivemosfet)

Infineon's leading

International Rectifier  
**IR** Automotive  
Power MOSFETs  
See more at [IRF.com](http://IRF.com)

**MOSFET Finder**

Quick Start Features More

Select Breakdown Voltage ▼

Drain Current  A

R<sub>DS(on)</sub>  mOhm

Reset Find Parts

Related Links

New OptiMOS™ 5 40V in  
S308 Package

TO-Leadless Package  
(TOLL)

Leading and Innovative  
Semiconductor Solutions  
for Hybrid and Electric  
Vehicles

Small Electric Vehicles  
MOSFETs

Package Outlines

Automotive MOSFET -  
Product Overview



# Automotive MOSFET

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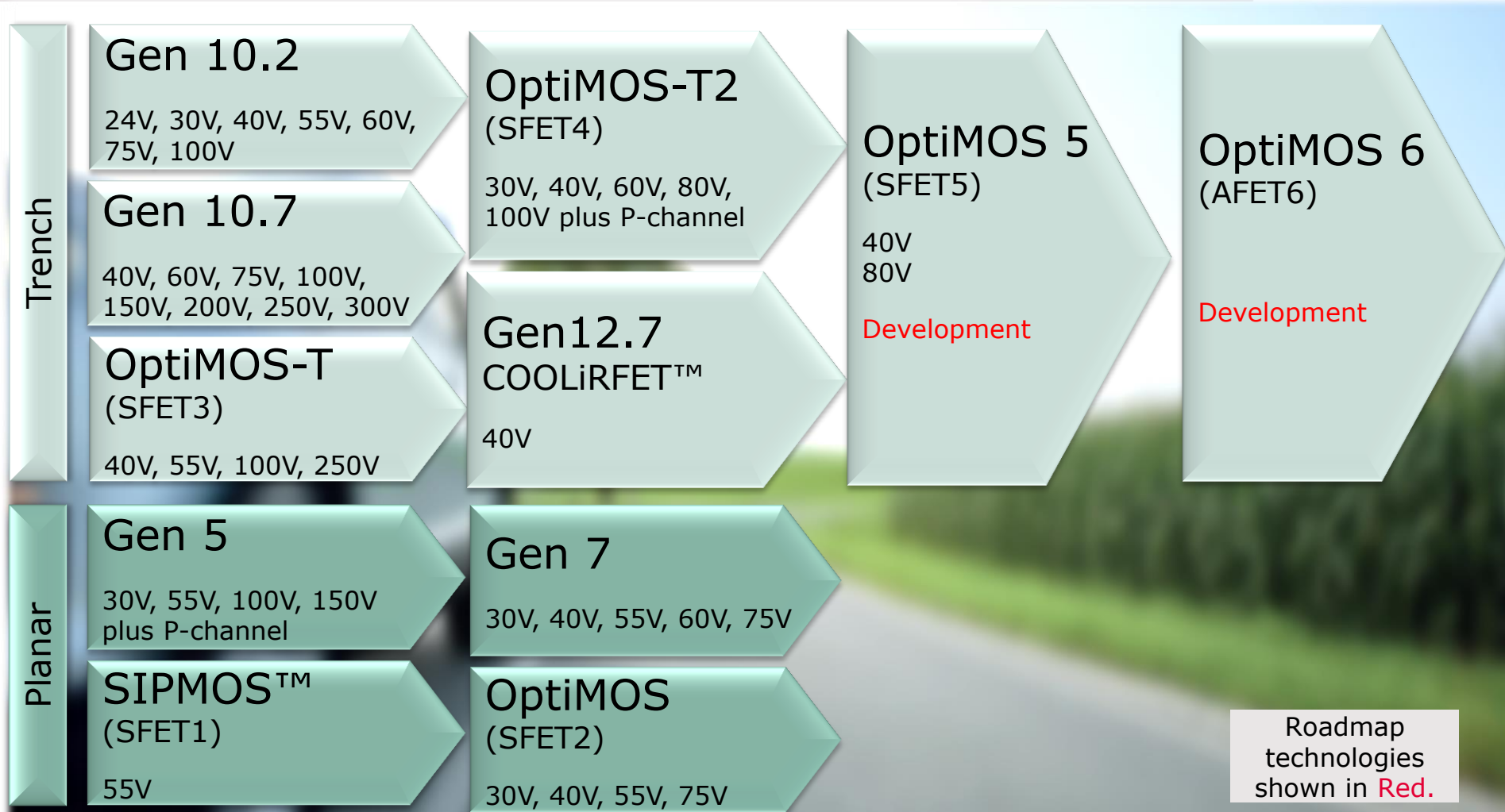
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# Process Technology Portfolio and Roadmap

## Infineon Technology Leadership in Trench Power MOSFETs



Roadmap technologies shown in Red.

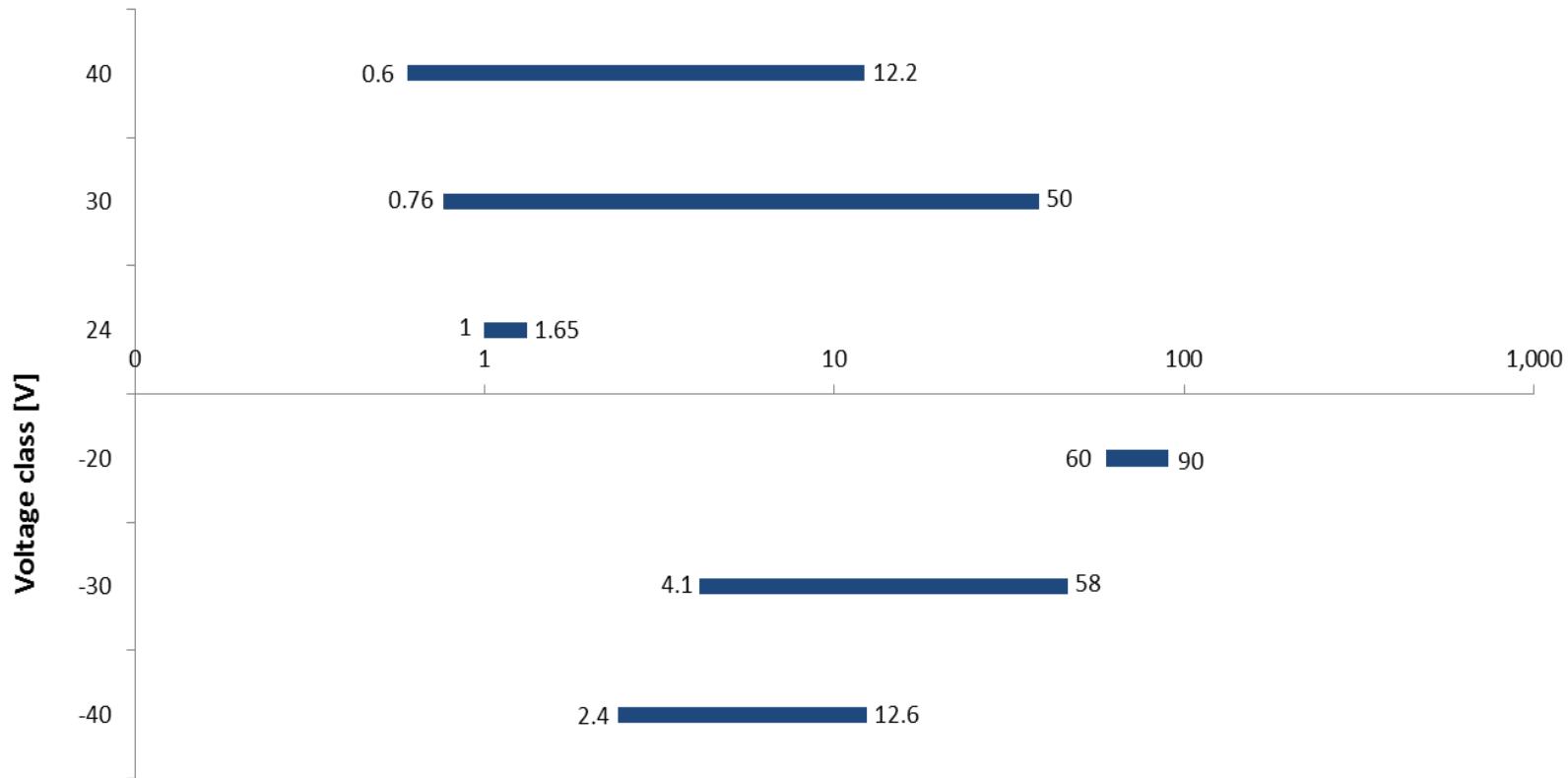
IFX offers planar and trench technologies to address the needs of the entire Automotive MOSFET Market:

- › SFET2, Gen7, Gen5 → Planar → Thermal/R<sub>th</sub> driven applications
- › SFET5, SFET4, Gen12.7, SFET3, Gen10.7, Gen10.2 → Trench → R<sub>ds(on)</sub> driven applications

# Packages available by voltage class vs. RDS(on) N and P-Channel 20V to 40V



N + P-Channel  $\leq 40V$

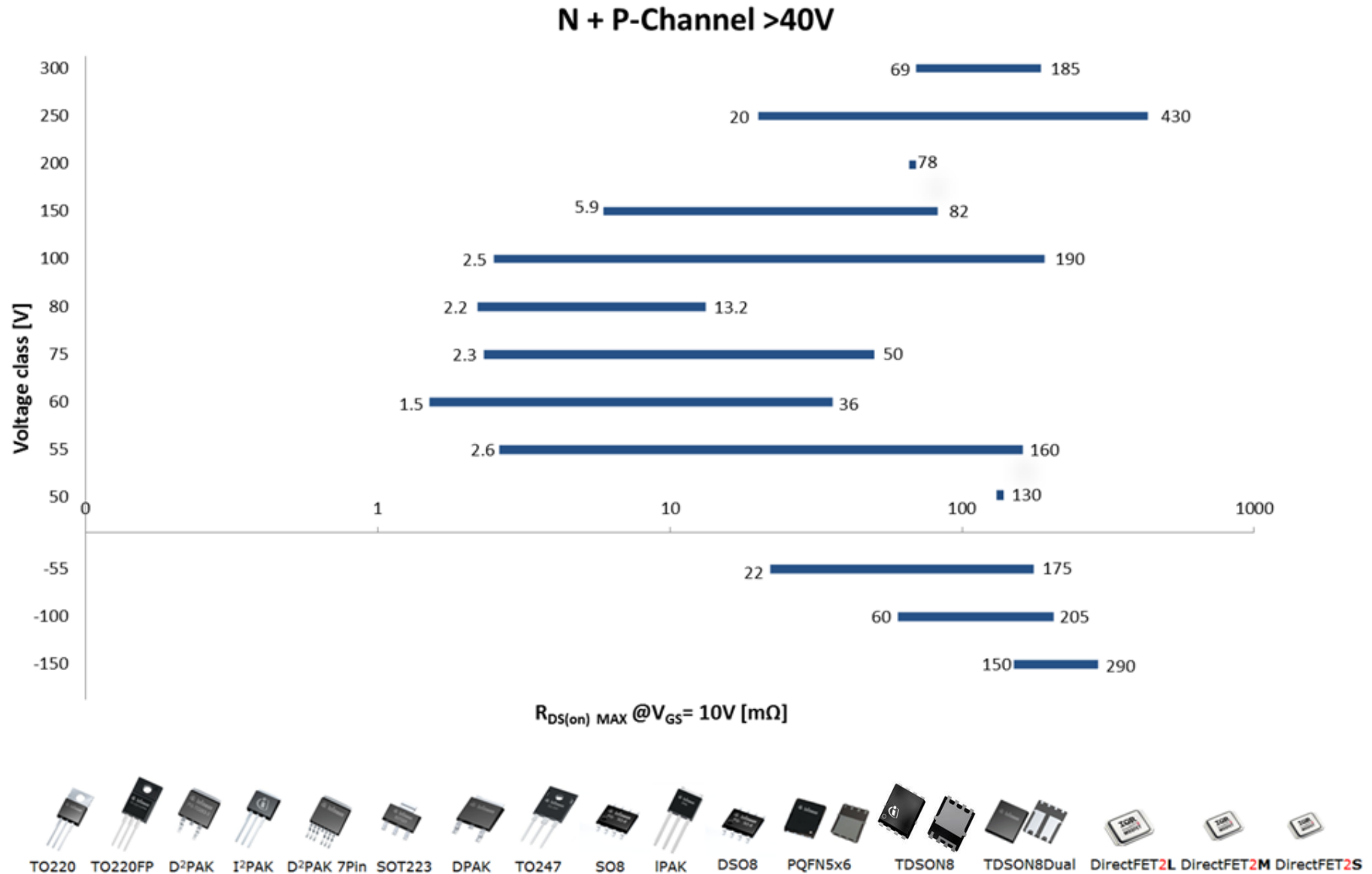


$R_{DS(on)} \text{ MAX @ } V_{GS} = 10V \text{ [m}\Omega\text{]}$





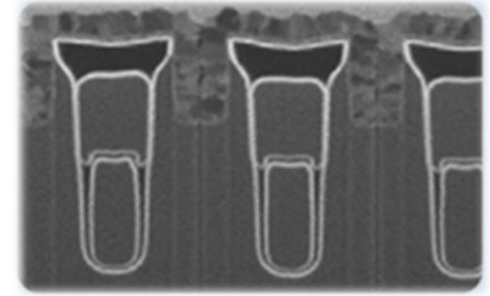
# Packages available by voltage class vs. RDS(on) N and P-Channel 50V to 300V



OptiMOS-5 (SFET5)  
Infineon's next-generation leading Trench  
Technology

- **Improved  $R_{DSon}$  \* Area**

- *Leading  $R_{DSon}$  and Low conduction losses*
- *Smaller packages*

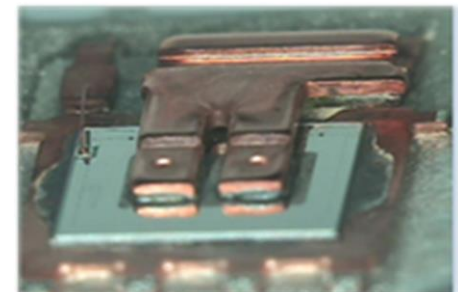


- **Improved Switching Parameters**

- *Reduced  $C_{iss}$  &  $C_{oss}$*
- *Improved switching behavior and EMC*

- **New Top-side Copper-Clip Contact Technology**

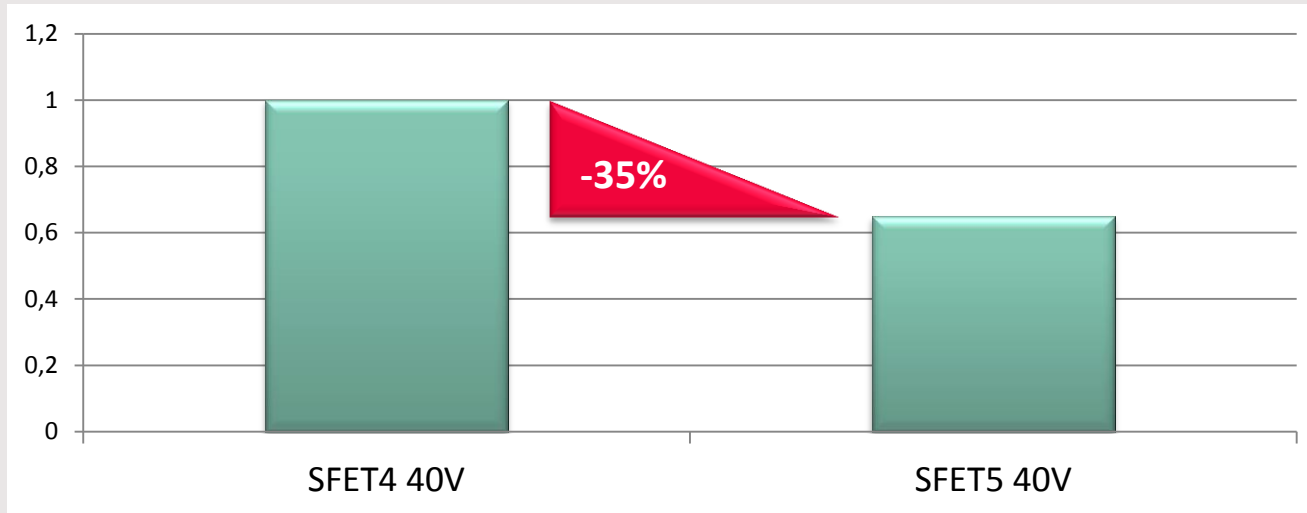
- *Smaller packages for same power*
- *Lower thermal resistance*
- *Leading current capability*



# An Advantage of OptiMOS™5

## Major Improvement in $R_{DS(on)}$ Resistance.

### ■ Technology Comparison — $R_{DS(on)}$ ·A



### ■ Product Comparison – BIC 40V in SS08 (5x6)

Parameter	IPC100N04S4-02	IPC100N04S5-1R2	SS08 S5 40V vs S4 40V
$R_{DS(on)}$ [mΩ]	2.4	1.2	50% Reduction in Conduction Losses
$Q_G$ [nC]	79	73	Constant $Q_g$ at 50% $R_{DS(on)}$ Reduction
FOM [nC·mΩ]	190	88	50% Better FOM

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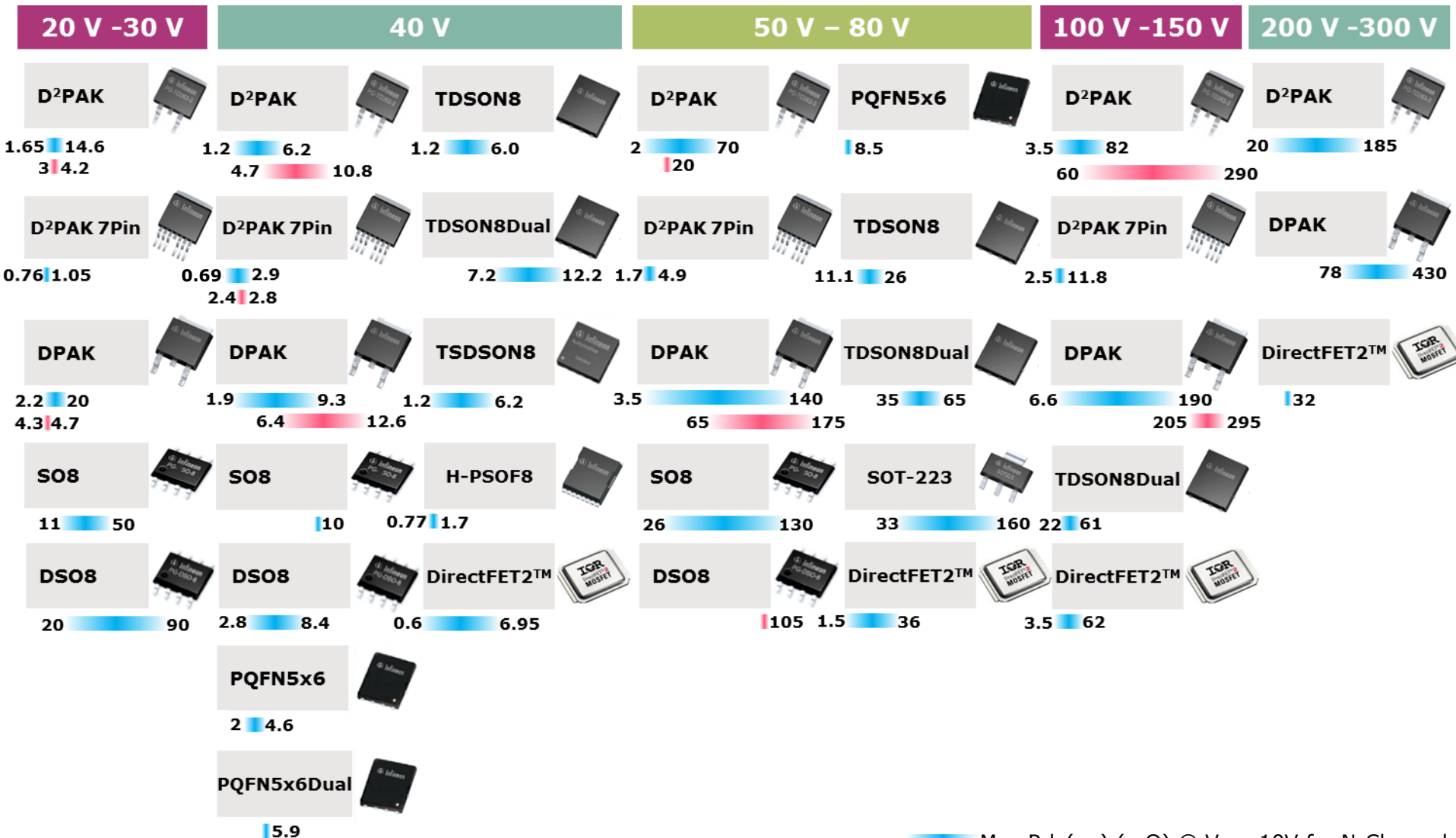
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# Surface Mount Packages

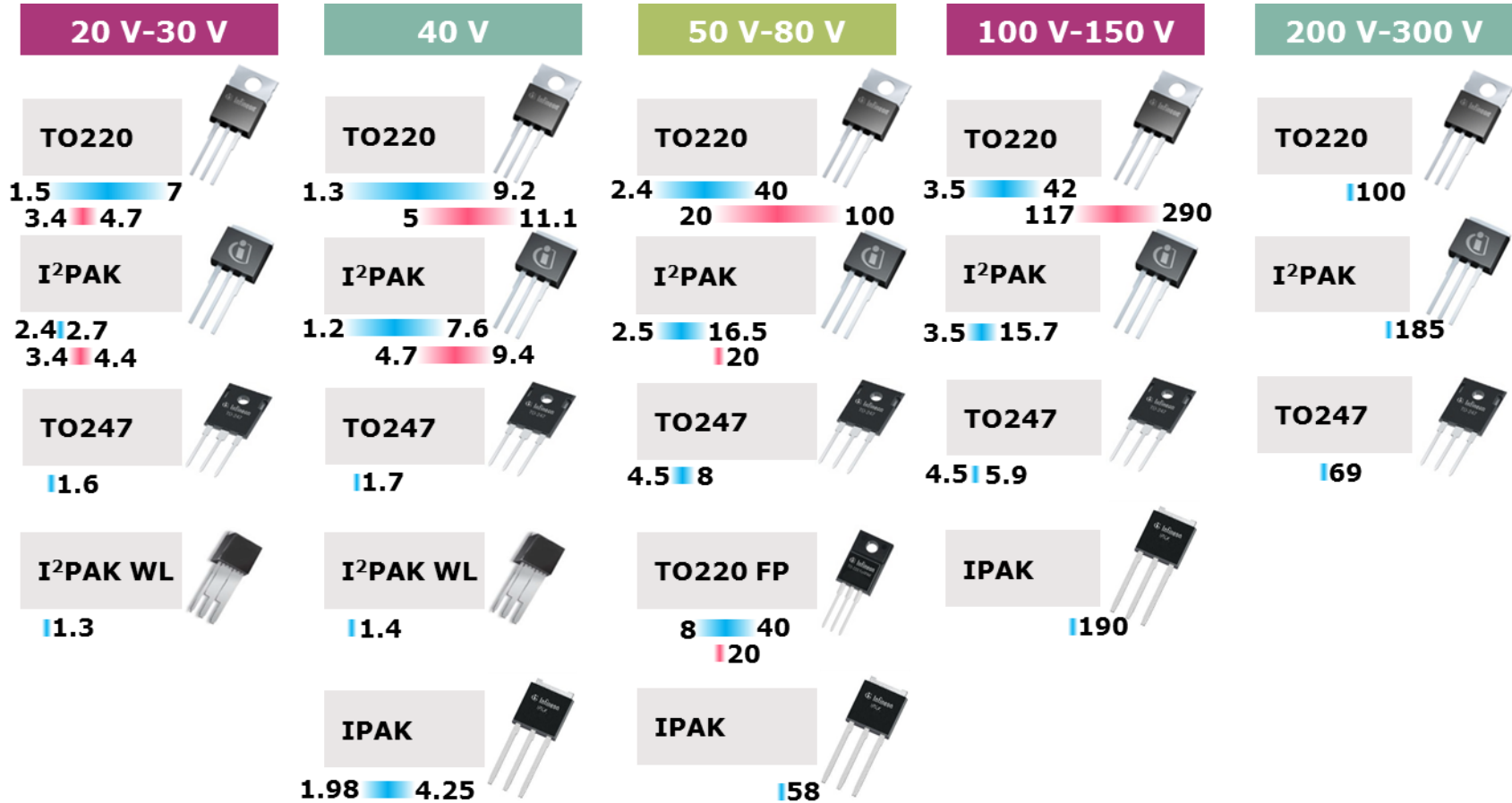
## Rds(on) Range by Voltage Class



Max Rds(on) (mΩ) @ Vgs=10V for N-Channel  
 Max Rds(on) (mΩ) @ Vgs=10V for P-Channel

# Through Hole Packages

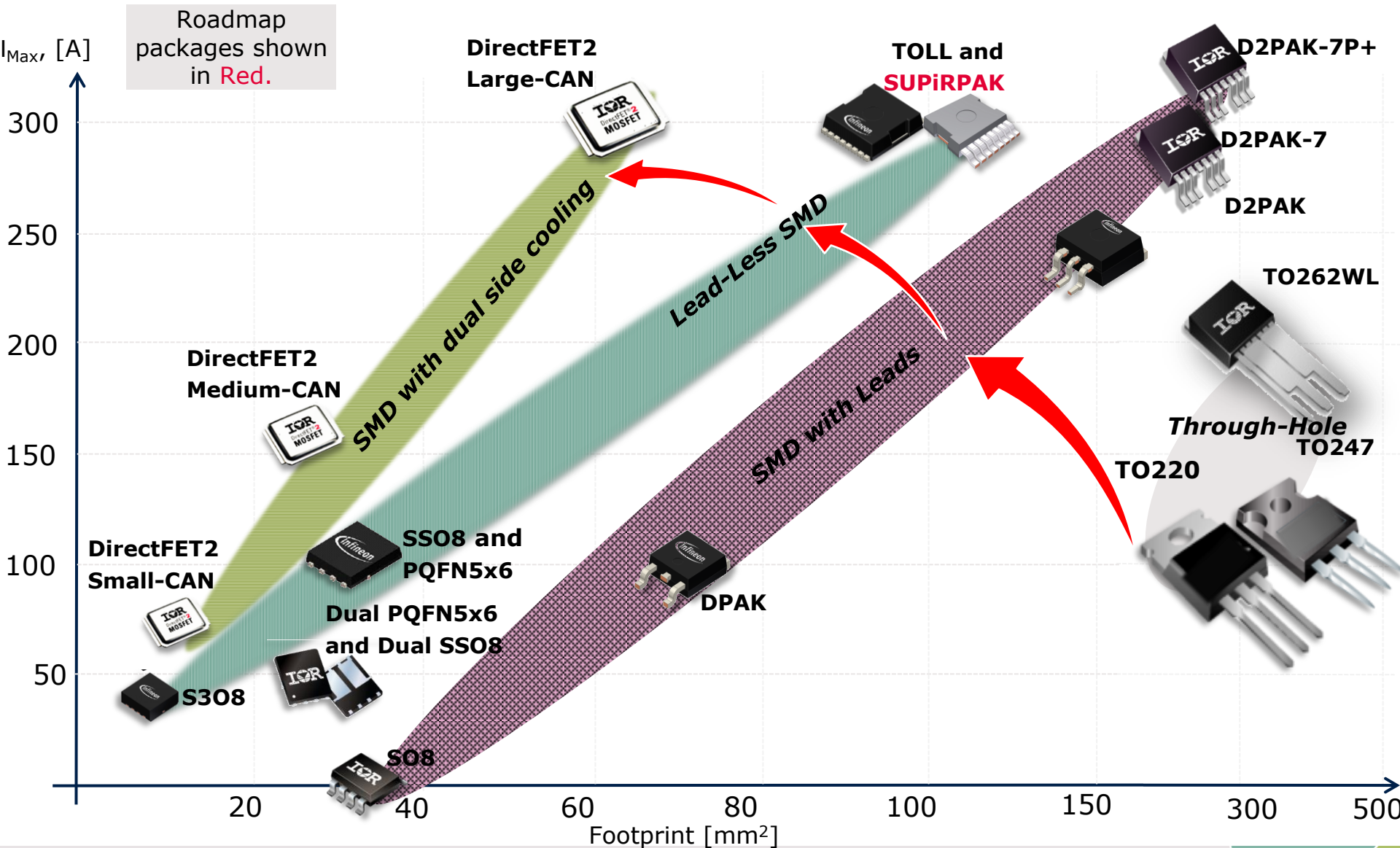
## Rds(on) Range by Voltage Class



Max Rds(on) (mΩ) @ Vgs=10V for N-Channel  
 Max Rds(on) (mΩ) @ Vgs=10V for P-Channel

# Package Portfolio and Roadmap

## Advancing cooling and miniaturization





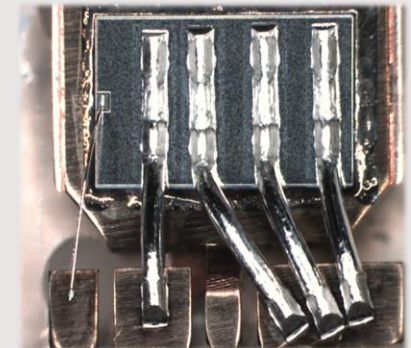
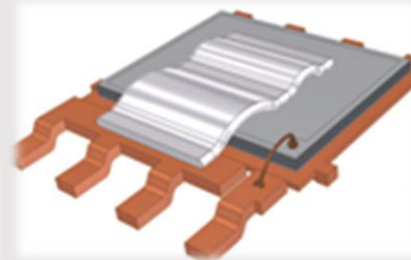
# Automotive MOSFET Package Development

## › Connecting Technologies Improvements

- › Copper clips, ribbon bond, thicker bond wires, diffusion soldering, etc.
- › DirectFET2®, TO-262 WideLead, D2PAK-7P+

## › Benefits:

- › Higher current capabilities
- › Better thermal performance
- › 100% Pb free products



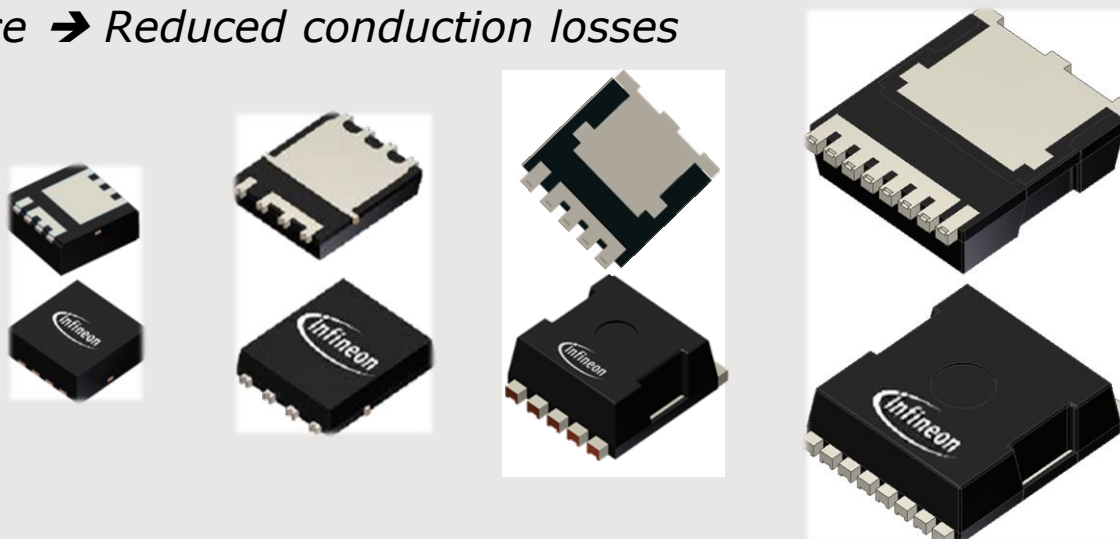
# Automotive MOSFET Package Development

## › Development of new Leadless Packages

- › Shrink Super SO8 (S308)
- › Dual & Single Super SO8 (SS08)/PQFN5x6
- › TOLL (TO-Leadless)
- › SUPiRPAK

## › Features and Benefits:

- › *Smaller → Less PC Board space required. Possible cost savings.*
- › *Lower Resistance → Reduced conduction losses*



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# Infineon Automotive MOSFET Products

3x3mm Packages  
Shrink Super SO8 (S3O8)

# Shrink Super SO8 (S3O8) Automotive Package 40V Product Portfolio



Sales Name	Technology	max Ron 10V (mΩ)	ID (A)	LL/NL
IPZ40N04S5L-2R8	OptiMOS-5 40V	2.8	40	LL
IPZ40N04S5-3R1	OptiMOS-5 40V	3.1	40	NL
IPZ40N04S5L-4R8	OptiMOS-5 40V	4.8	40	LL
IPZ40N04S5-5R4	OptiMOS-5 40V	5.4	40	NL
IPZ40N04S5L-7R4	OptiMOS-5 40V	7.4	40	LL
IPZ40N04S5-8R4	OptiMOS-5 40V	8.4	40	NL

- › Only requires one-sixth the PCB area of a DPAK
- › Two devices take less PCB area than one Dual SS08
- › Uses benchmark OptiMOS-5 technology → Very low  $R_{DSon}$
- › 40A current rating in a very small package

› **In production now!**

## S3O8



L x W x H

3.3x3.3x1.0mm<sup>3</sup>

BiC 40V

**2.8mΩ**

$I_{DC} = \mathbf{40A}$

Cu-Clip soldered

# Infineon Automotive MOSFET Products

## 5x6mm Packages

- › Single Super SO8 (single SS08)
- › PQFN5x6

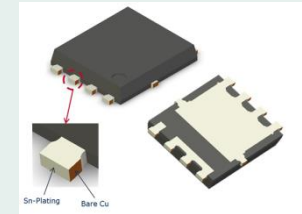
# Single Super SO8 Automotive-Qualified Package

## 40V Next Generation Product Portfolio in OptiMOS-5



Sales Name	max Ron 10V (mOhm)	ID (A)	LL/NL
IPC100N04S5-1R2	1.2	100	NL
IPC100N04S5L-1R1	1.1	100	LL
IPC100N04S5-1R7	1.7	100	NL
IPC100N04S5L-1R5	1.5	100	LL
IPC100N04S5-1R9	1.9	100	NL
IPC100N04S5L-1R9	1.9	100	LL
IPC100N04S5-2R8	2.8	100	NL
IPC100N04S5L-2R6	2.6	100	LL
IPC90N04S5-3R6	3.6	90	NL
IPC90N04S5L-3R3	3.3	90	LL
IPC70N04S5-4R6	4.6	70	NL
IPC70N04S5L-4R2	4.2	70	LL
IPC50N04S5-5R8	5.8	50	NL
IPC50N04S5L-5R5	5.5	50	LL

### Single SS08



L x W x H

6.5x5.1x1.0mm<sup>3</sup>

BiC 40V

**1.2mΩ**

**50% Rds(on) Reduction  
compared to SFET4 BiC !**

I<sub>DC</sub> = **100A**

Cu-Clip soldered

› **Available now!**

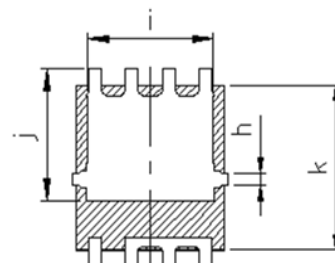
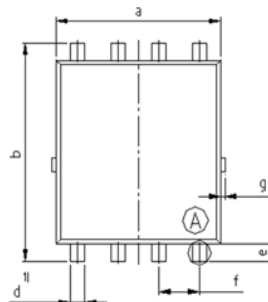
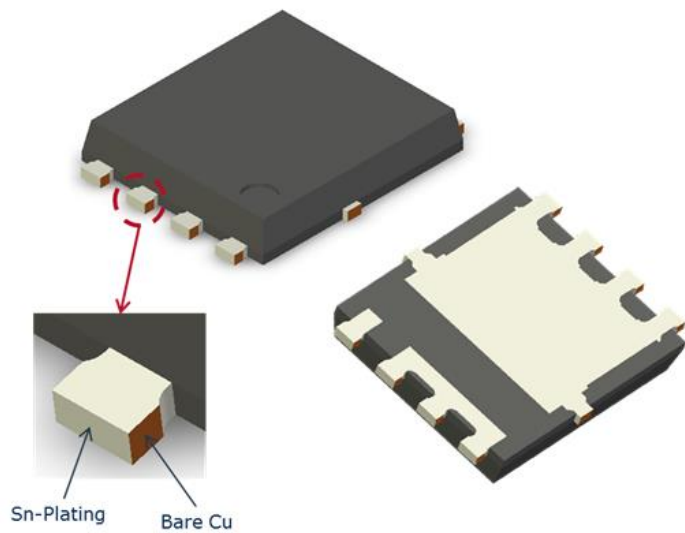
# OptiMOS-5 SSO8 Package Features and Benefits



- › Full 1000 hour life at the max  $T_j$  of 175C.
- › New lead-frame for better AOI feature. Complete front surface of the lead has tin plating.
- › No Source bond wires, but Cu clip connection for better resistance and thermal performance
- › Fused Source connection for better current density than competitors with only three isolated Source pins
- › Can replace a DPAK and Save half of the PCB area



# OptiMOS-5 SSO8 Package Detail



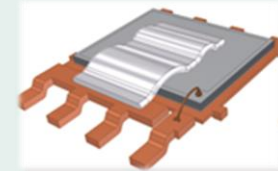
Characteristic		Target nominal dimension (mm)
Package X	a	5.15
Package Y	b	6.44
Package thickness	c	1.00
Lead width	d	0.44
Lead length	e	0.48
Lead pitch	f	1.27
Fish tail length	g	0.13
Fish tail width	h	0.40
Heat sink X	i	4.30
Heat sink Y	j	4.30
Molded Body	k	5.48

# PQFN56 Automotive-Qualified High Voltage MOSFET Portfolio

Device	Vds (V)	Technology	max Ron (mΩ)	ID (A)	Package	LL/NL
AUIRFN7107TR	75	Gen10.7	8.5	76	PQFN56	NL
AUIRFN7110TR	100	Gen10.7	14.5	58	PQFN56	NL

- › *Can replace a DPAK in only half the PCB area*
- › *Low  $R_{DSon}$  → low conduction losses*
- › ***In production now!***

## PQFN56

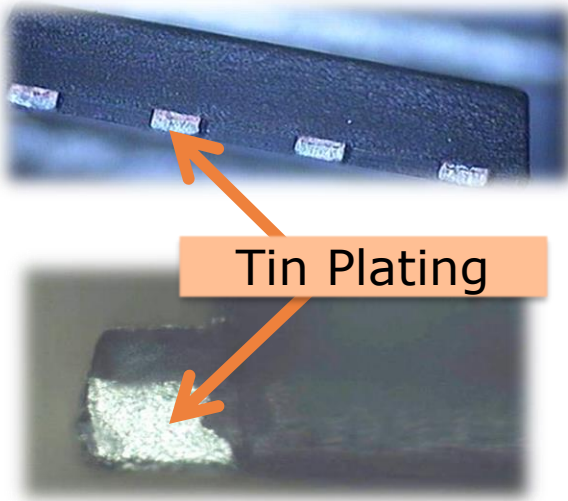


**Al ribbon wire**

# PQFN56 Leadless Package

## Extended Lead with End Lead Plating

### Side View

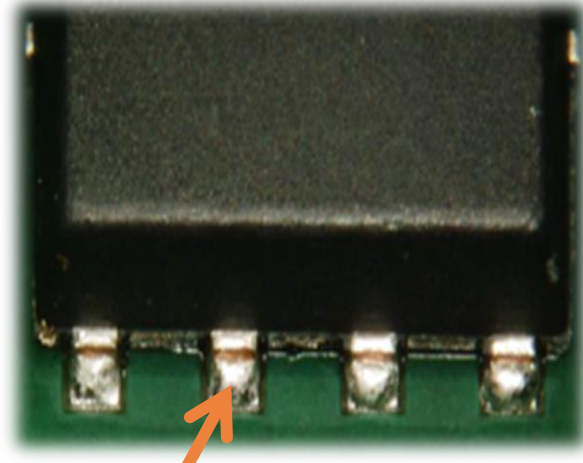


Tin Plating

End Lead Plating Ensures  
Good Solderability

AOI - Inspectable Solder  
Joint

### Solderability Testing Results



Good Solder Cover  
Range at End Lead

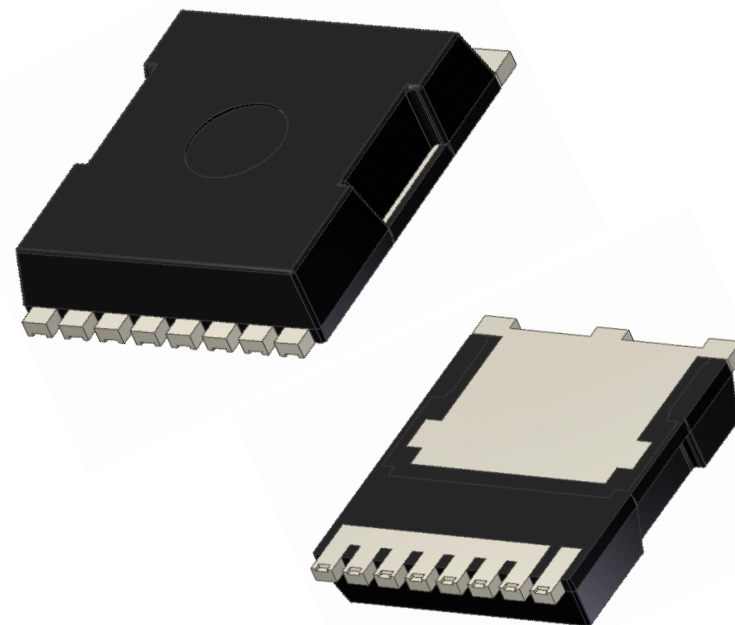
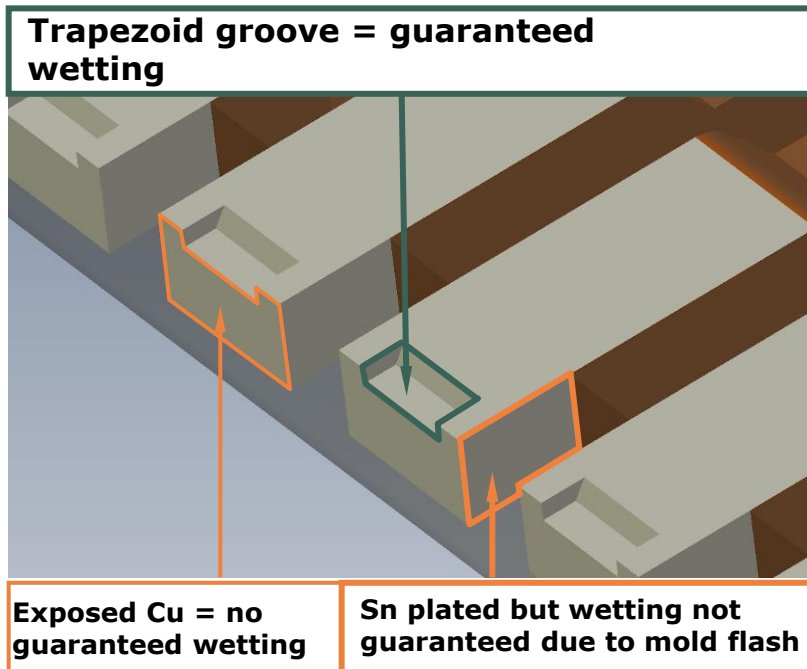
Solder Across the Lead  
from Side to Side

# Infineon Automotive MOSFET Products

TOLL (TO Lead-Less) package products

# TO-Leadless Package Features

- › 30% less board space compared with D<sup>2</sup>PAK
- › Reduced package electrical and thermal resistance
- › Lower lead inductances for optimized switching performance
- › *Compatible with AOI at your manufacturing site*



# OPTIMOS-T2 40V TOLL

## Automotive Product Portfolio

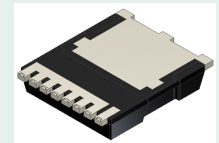
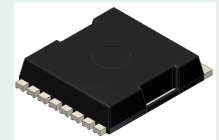


### › 40V Product Family

Product Name	Technology	max Ron 10V (mΩ)	ID (A)	LL/NL	SOP
IPLU300N04S4-R8	OptiMOS-T2 40V	0.77	300	NL	released
IPLU300N04S4-1R1	OptiMOS-T2 40V	1.1	300	NL	released
IPLU250N04S4-1R7	OptiMOS-T2 40V	1.7	250	NL	released

› ***In production now!***

### TOLL



L x W x H  
11.7x9.9x2.3mm

BiC 40V  
**0.77mΩ**

$I_{DC} = \mathbf{300A}$

Al-bond wire

# NEW OPTIMOS-5 80V TOLL

## Automotive Product Portfolio



Device	Technology	max Ron (mOhm)	ID (A)	Package	LL/NL
IAUT300N08S5N012	OptiMOS 5 80V	1.2mOhm	300	TOLL	NL
IAUT165N08S5N029	OptiMOS 5 80V	2.9mOhm	165	TOLL	NL

› **Available now!**

### TOLL



L x W x H  
11.7x9.9x2.3mm

BIC 80V  
**1.2mΩ**

$I_{DC} = 300A$

Al bond wire

# NEW OPTIMOS-5 100V TOLL

## Automotive Product Portfolio



Device	Technology	max Ron (mOhm)	ID (A)	Package	LL/NL
IAUT300N10S5N015	OptiMOS 5 100V	1.50	300	TOLL	NL
IAUT150N10S5N035	OptiMOS 5 100V	3.50	150	TOLL	NL

SFET5 100V TOLL	Project schedule
Deliverable	
ES	Available
QS	Q3 2017
SOP	Q1 2018

**Preliminary Information. Subject to change.**

\* Please contact Product Marketing if you require urgent sampling support

### TOLL



L x W x H  
11.7x9.9x2.3mm

BIC 100V  
**1.5mΩ**

$I_{DC} = \mathbf{300A}$

Al bond wire



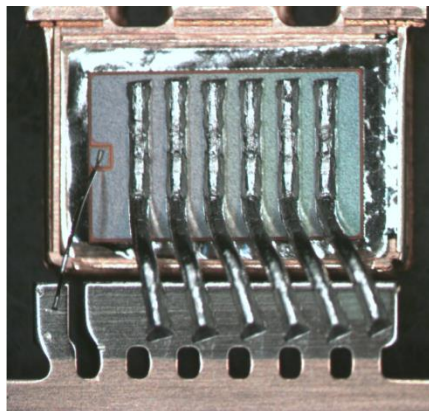
# Infineon Automotive MOSFET Products

D2PAK-7P+ package  
products

# **NEW!** D2PAK-7P+ Package Feature

## Same Footprint, 50% More Current!

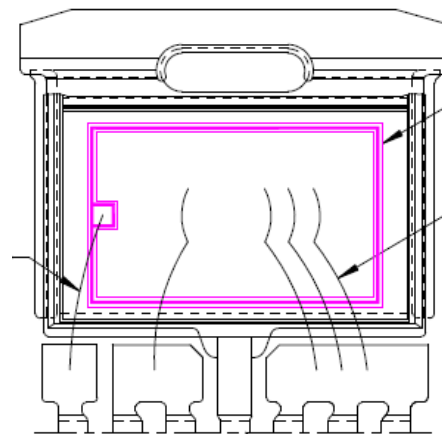
**D2PAK-7P+**



**Maximum  
6 x 500µm Wire**

**360A**

**D2PAK-7P**



**Maximum  
4 x 500µm Wire**

**240A**

**Footprint 100% Compatible**

# D2PAK-7P+ 40V Product Portfolio

Device	Technology	max Ron (mΩ)	ID (A)	Package	LL/NL
AUIRFS <sup>A</sup> 8409-7P	Gen12.7 40V	0.69	360	D2PAK-7P+	NL

**In Production Now!**

Same D2PAK-7P footprint

Drop in replacement

Up to 360A current

Improved power density

Benchmark  $R_{ds(on)}$

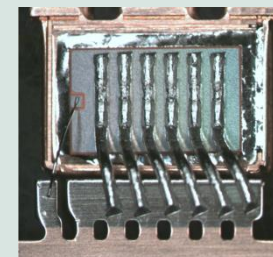
Product Datasheet  
available online:

<http://www.irf.com/product-info/datasheets/data/auirfsa8409-7p.pdf>

Targeted Applications

- Battery Switch
- Reverse Battery Protection
- EPS Phase Switch

## D2PAK-7P+



L x W x H  
15.2.7x10.2x4.4mm  
(Same as D2PAK-7P)

BIC 40V  
**0.69mΩ**

$I_{DC} = 360A$

Al bond wire

# Infineon Automotive MOSFET Products

Dual Super SO8 (Dual SS08)

# Dual Super-SO8/PQFN56 Product Family Planar & Trench, 40V/60V/100V Products

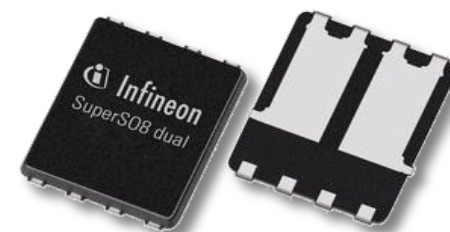


› Can replace two DPAKs while taking 75% less PCB area

OptiMOS Dual SSO8	Technology	max Ron 10V (mOhm) / channel	ID (A) / channel	max RthJC [K/W]	LL/NL	Package
AUIRFN8459	Gen12.7 40V	5.9	50	3.0	NL	PQFN56 Dual
IPG20N04S4L-07A	OptiMOS-T2 40V	7.2	20	2.3	LL	PG-TDSON-8-10
IPG20N04S4-08A	OptiMOS-T2 40V	7.6	20	2.3	NL	PG-TDSON-8-10
IPG20N04S4L-08A	OptiMOS-T2 40V	8.2	20	2.8	LL	PG-TDSON-8-10
IPG20N04S4L-11A	OptiMOS-T2 40V	11.6	20	3.7	LL	PG-TDSON-8-10
IPG20N04S4-12A	OptiMOS-T2 40V	12.2	20	3.7	NL	PG-TDSON-8-10
IPG20N06S4L-11A	OptiMOS-T2 60V	11.2	20	2.3	LL	PG-TDSON-8-10
IPG20N06S4L-14A	OptiMOS-T2 60V	13.7	20	3.0	LL	PG-TDSON-8-10
IPG20N06S4-15A	OptiMOS-T2 60V	15.5	20	3.0	NL	PG-TDSON-8-10
IPG20N06S4L-26A	OptiMOS-T2 60V	26.0	20	4.5	LL	PG-TDSON-8-10
IPG20N06S2L-35A	OptiMOS 55V	35.0	20	2.3	LL	PG-TDSON-8-10
IPG20N06S2L-50A	OptiMOS 55V	50.0	20	2.9	LL	PG-TDSON-8-10
IPG20N06S2L-65A	OptiMOS 55V	65.0	20	3.5	LL	PG-TDSON-8-10
IPG20N10S4L-22A	OptiMOS-T2 100V	22.0	20	2.2	LL	PG-TDSON-8-10
IPG20N10S4L-35A	OptiMOS-T2 100V	35.0	20	3.2	LL	PG-TDSON-8-10
IPG20N10S4-36A	OptiMOS-T2 100V	35.0	20	3.2	NL	PG-TDSON-8-10
IPG16N10S4-61A	OptiMOS-T2 100V	61.0	16	4.8	NL	PG-TDSON-8-10
IPG16N10S4L-61A	OptiMOS-T2 100V	61.0	16	5.2	LL	PG-TDSON-8-10

› **Front-end technology options:**

- OptiMOS™ 55V: Thermal driven applications
- OptiMOS™-T2 40V:  $R_{DSon}$  driven applications
- Gen12.7 40V:  $R_{DSon}$  driven applications
- OptiMOS™-T2 60V:  $R_{DSon}$  driven applications
- OptiMOS™-T2 100V:  $R_{DSon}$  driven applications
- AOI-compatible leads (a.k.a. wettable flanks)
- Standard leads w/o AOI at lower price



# Infineon Automotive MOSFET Products

DirectFET<sup>2</sup>® -

Increase System Efficiency and Power Density



# DirectFET<sup>2</sup>® - Improve System Efficiency, Reliability and Power Density




Very Small Package Size and Height

Lowest Package Resistance and Inductance

Lowest Top-Side Thermal Impedance

No Lead Frame, Wire Bond, or Molding

100%  , Solution to Address



Increased System Efficiency

Increased power Density

Reduced Parasitic Ringing

# DirectFET<sup>2</sup>® Success Story

**End Car Manufacturer:**  
Major OEM

**Application:**  
Adaptive Steering System

**Production Date:**  
2015



**More System Information:**  
[Link](#)

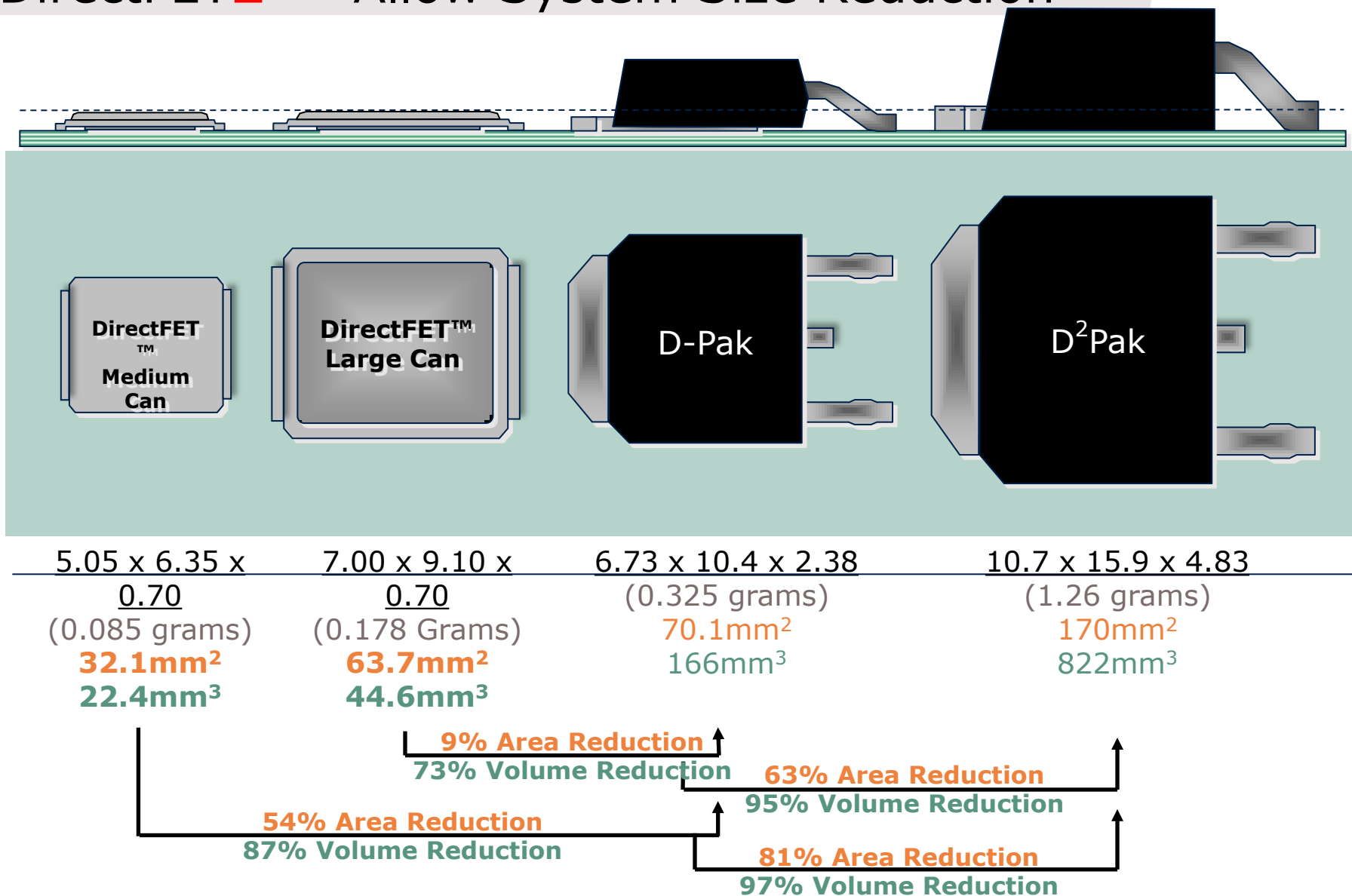
**Part Numbers:**  
40V 3mOhm Medium-Can AUIRF7736M2  
(6 per board)

## Key Factors for Design Win:

- ✓ Good reliability, ideal for automotive applications
- ✓ Small footprint - **DirectFET<sup>2</sup>** Medium Can is 54% smaller than DPAK
- ✓ Better thermal performance vs. conventional packages



# DirectFET<sup>2</sup>® - Allow System Size Reduction



# DirectFET<sup>2</sup>® Released Portfolios

Part numbers	Voltage (V)	max Ron 10V (mOhm)	QG typ (nC)	ID (A)	Package	NL/LL	Optimized Feature
AUIRF8739L2	40	0.6	362	309	Large Can - L8	NL	Low Rds(on)
AUIRF7739L2	40	1.0	220	270	Large Can - L8	NL	Low Rds(on)
AUIRF7738L2	40	1.6	147	210	Large Can - L6	NL	Low Rds(on)
AUIRF8736M2	40	1.9	136	137	Medium Can - M4	NL	Low Rds(on)
AUIRF7737L2	40	1.9	89	156	Large Can - L6	NL	Low Rds(on)
AUIRF7736M2	40	3.0	72	108	Medium Can - M4	NL	Low Rds(on)
AUIRL7736M2	40	3.0	59	143	Medium Can - M4	LL	Low Rds(on)
AUIRF7734M2	40	4.9	48	72	Medium Can - M2	NL	Low Rds(on)
AUIRL7732S2	40	6.6	24	65	Small Can - SC	LL	Low Rds(on)
AUIRF7732S2	40	7.0	30	55	Small Can - SC	NL	Low Rds(on)
AUIRF7749L2	60	1.6	200	200	Large Can - L8	NL	Low Rds(on)
AUIRF7648M2	60	7.0	35	68	Medium Can - M4	NL	Low Qg
AUIRF7640S2	60	36.0	7	21	Small Can - SB	NL	Low Qg
AUIRF7759L2	75	2.3	200	160	Large Can - L8	NL	Low Rds(on)
AUIRF7769L2	100	3.5	200	124	Large Can - L8	NL	Low Rds(on)
AUIRF7669L2	100	4.4	81	114	Large Can - L8	NL	Low Qg
AUIRL7766M2	100	10.0	44	51	Medium Can - M4	LL	Low Rds(on)
AUIRF7647S2	100	31.0	14	24	Small Can - SC	NL	Low Qg
AUIRF7665S2	100	62.0	8	14	Small Can - SB	NL	Low Qg
AUIRF7675M2	150	56.0	21	18	Medium Can - M2	NL	Low Qg
AUIRF7799L2	250	32.0	110	35	Large Can - L8	NL	Low Rds(on)



# Automotive MOSFET

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# Infineon Automotive Power MOSFET Conclusion



## Vision: Your preferred automotive MOSFET supplier

### Our leading market position today

- › Benchmark OptiMOS™-5 trench technology:  $R_{ds(on)} < 1m\Omega$ . *Better energy efficiency & potential space savings*
- › Excellent *Application Engineering* support for your R&D
- › Zero Defect program: *Quality focus. Lower total costs.*
- › Broad portfolio of automotive qualified devices
  - Planar and trench technology devices
  - N- and P-channel MOSFET devices
  - Standard and new innovative packages
  - Products available from 24V to 300V
  - The best from IR and Infineon brought together
  - *Hundreds of part numbers to choose for your design*
- › Leadership on legal/green requirements: offering the first 100% Pb-free devices. *Supporting your future needs.*

### Our plans to support you in the future

- › Focus on new packages
  - Integration – *Space savings (PCB area/cost)*
  - Smaller packages – *Space savings*
  - High current devices – *Capability to drive larger loads. Savings potential by eliminating parallel devices.*
- › Next OptiMOS™-5 trench technology
  - *even better performance for conduction energy efficiency & improved EMC switching behavior*
  - *enables innovative packages*
- › Latest product information & support on our homepage:  
**[www.infineon.com/automotivemosfet](http://www.infineon.com/automotivemosfet)**

# Automotive MOSFET Naming System

**I** **P** **D** **90** **N** **06** **S4** **L** - **05**

**I** → Infineon

Device:

**P** for Power-MOSFET

**T** for Twin Power-MOSFET  
(Common Drain)

Package Type:

**B** for TO263/D<sup>2</sup>PAK

**C** for SuperSO8 (TDSO8-08)

**D** for TO252/DPAK

**I** for TO262/I<sup>2</sup>PAK

**LU** for TO-Leadless (H-PSOF)

**P** for TO220

**G** for Dual SuperSO8 (TDSO8-08)

**Z** for Shrink SuperSO8 (TSDSO8-08)

**R<sub>DSon,max</sub>** in mΩ  
"H" in front  
for higher ohmic version  
i.e. H5=5.5mΩ  
"R" as decimal separator  
i.e. 1R3=1.3mΩ

**L** for Logic Level  
(no L is Normal Level)

**S** for SFET1

**S2** for OptiMOS™

**S3** for OptiMOS™-T

**S4** for OptiMOS™-T2

**S5** for OptiMOS™-5

**P3** for PFET3 Trench

**P4** for PFET4 Trench

Breakdown voltage  $V_{BrDSS} \div 10$

**P** for p-channel

**N** for n-channel

Continuous drain current  $I_{Dmax}$

# Former IRF Gen12.7 COOLiRFET™ Naming System



AUIRFS8409 xxx

## Automotive Grade

AEC-Q101

## IR HEXFET

IRF → Standard gate

IRL → Logic Level gate

## Package Type

S = D2PAK

S-7P = D2PAK-7P

SL = TO-262

WL (suffix) = TO262 Wide Lead

B = TO-220

N = PQFN

R = DPAK

U = TO-252 (IPAK)

C = Die Sale

## Die Size

## For Die Sale:

B = Whole Wafer

D = Chip Pack

F = Die on Film

## For Packaging Options:

KD = Known Good Die in Chip Pack

KT = Known Good Die in Tape & Reel

Blank = Tube

TR = Tape & Reel

TR(R,L) = Tape & Reel (Right, Left)

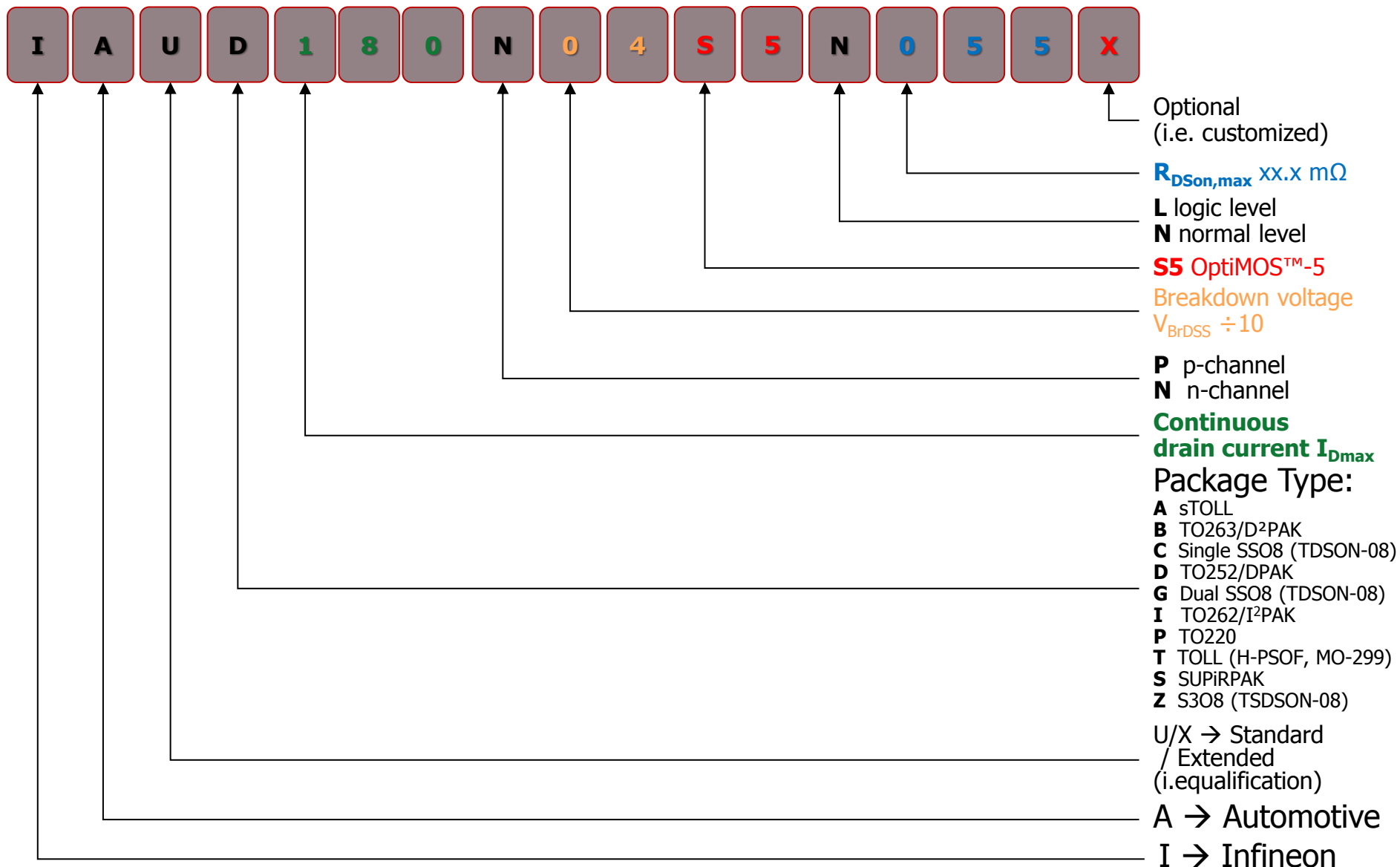
## Product Family

8 → Gen 12.7

## Voltage Rating

4 → 40V

# Future Automotive MOSFET Naming System







Part of your life. Part of tomorrow.

