


**PRODUCT / PROCESS CHANGE INFORMATION**

**1. PCI basic data**

<b>1.1 Company</b>		STMicroelectronics International N.V
<b>1.2 PCI No.</b>		MMS/16/9718
<b>1.3 Title of PCI</b>		STM8AF product family errata sheets update
<b>1.4 Product Category</b>		STM8AF product family
<b>1.5 Issue date</b>		2016-04-28

**2. PCI Team**

<b>2.1 Contact supplier</b>	
<b>2.1.1 Name</b>	GOODMAN ROBERT
<b>2.1.2 Phone</b>	+1 6024856271
<b>2.1.3 Email</b>	robert.goodman@st.com
<b>2.2 Change responsibility</b>	
<b>2.2.1 Product Manager</b>	Michel BUFFA
<b>2.1.2 Marketing Manager</b>	Daniel COLONNA
<b>2.1.3 Quality Manager</b>	Pascal NARCHE

**3. Change**

<b>3.1 Category</b>	<b>3.2 Type of change</b>	<b>3.3 Manufacturing Location</b>
General Product & Design	Modification of datasheet	NA

**4. Description of change**

	Old	New
<b>4.1 Description</b>	<p>Previous errata sheets:</p> <ul style="list-style-type: none"> <li>- Errata sheet DocID18392 revision 3 for STM8AF622x/4x/66/68, STM8AF612x/4x/66/68 products</li> <li>- Errata sheet DocID18393 revision 5 for STM8AF5xxx STM8AF6xxx products</li> <li>- Errata sheet DocID025112 revision 1 for STM8AF6223 STM8AF6226T products</li> </ul> <p>The product datasheets below, specify VDD rise time parameter, implicitly for a VDD starting from 0V:</p> <ul style="list-style-type: none"> <li>- Datasheet DocID14395 STM8AF526x/8x/Ax STM8AF6269/8x/Ax revision 12,</li> <li>- Datasheet DocID14952 STM8AF6246 STM8AF6248 STM8AF6266 STM8AF6268 revision 10,</li> <li>- Datasheet DocID025118 STM8AF6213 STM8AF6223 STM8AF6223A STM8AF6226 revision 5.</li> </ul>	<p>New errata sheets:</p> <ul style="list-style-type: none"> <li>- Errata sheet DocID18392 revision 4 for STM8AF624x STM8AF626x products</li> <li>- Errata sheet DocID18393 revision 6 for STM8AF52xx STM8AF62xx products</li> <li>- Errata sheet DocID025112 revision 2 for STM8AF6213 STM8AF6223/26 products</li> </ul>
<b>4.2 Anticipated Impact on form,fit, function, quality, reliability or processability?</b>	no change	

**5. Reason / motivation for change**

<b>5.1 Motivation</b>	<p>Errata sheets update to better specify VDD rise time rate conditions that may be encountered in very specific application use cases.</p> <p>The product datasheets specify VDD rise time parameter implicitly for a VDD starting from 0V. Some very specific applications could have a VDD starting from a residual voltage already above 0V and thus require that we explicitly specify these conditions.</p> <p>Even if such applications, cumulating a VDD starting from a residual voltage above 0V and VDD rise time rate slower than 50µs/V, could be affected by potential marginal start-up limited to the PPM range, we decided to inform our customers as a precautionary measure.</p> <p>Applications that are not presenting any residual voltage on VDD and having a VDD rise time rate faster than 50µs/V are not concerned by this errata sheet update.</p>
<b>5.2 Customer Benefit</b>	QUALITY IMPROVEMENT

<b>6. Marking of parts / traceability of change</b>	
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<b>6.1 Description</b>	NA
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<b>7. Timing / schedule</b>	
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<b>7.1 Date of qualification results</b>	2016-04-27
<b>7.2 Intended start of delivery</b>	2016-04-27
<b>7.3 Qualification sample available?</b>	Not Applicable

<b>8. Qualification / Validation</b>			
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<b>8.1 Description</b>			
<b>8.2 Qualification report and qualification results</b>	In progress	<b>Issue Date</b>	

<b>9. Attachments (additional documentations)</b>	
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9718PpPrdtLst.pdf
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<b>10. Affected parts</b>		
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10. 1 Current		10.2 New (if applicable)
10.1.1 Customer Part No	10.1.2 Supplier Part No	10.1.2 Supplier Part No
	STM8AF5288TDY	
	STM8AF52A8TAX	
	STM8AF52A8TCX	
	STM8AF6246TASSSX	
	STM8AF6246TCSSSX	
	STM8AF6248TDX	
	STM8AF6248TDY	

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