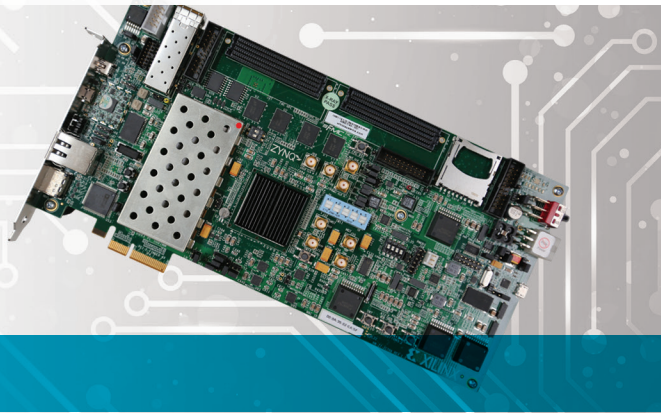
Xilinx Zynq-7000 All Programmable SoC ZC706 Evaluation Kit Optimized for JESD204B



Electronics Marketing Technology

Kit Includes
- ZC706 Evaluation Board featuring the XC7Z045 FFG900 -3 SoC
- Cables & Power Supply

Target Apps
- Wideband data acquisition over JESD204B at maximum line rate of 12.5 Gb/s
- Test and measurement equipment
- Radar and advanced imaging
- CMTS and Cable Access
- Military/Aerospace

The Zynq®-7000 All Programmable SoC ZC706 Evaluation Kit Optimized for JESD204B provides a platform for high-speed analog data acquisition over JESD204B at maximum line rates of 12.5 Gb/s. High-level design methodologies, IP, and verified reference designs are available to connect high-speed analog FMC cards with JESD204B through the GTX gigabit serial transceivers of the Zynq-7000 All Programmable SoC.

Features

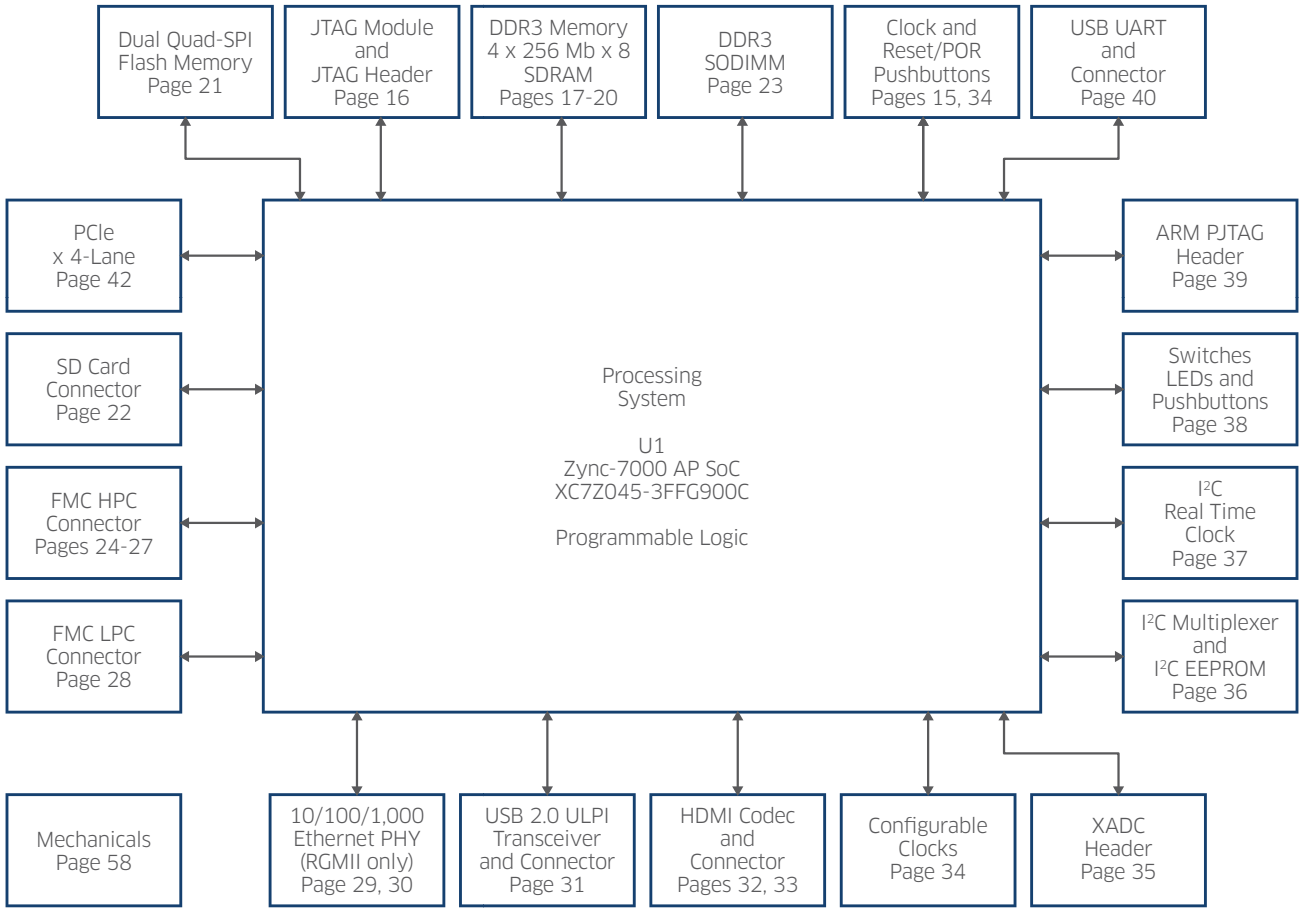
- ROHS compliant ZC706 kit featuring the Zynq-7000 AP SoC XC7Z045 FFG900 – 3
- Configuration
  - Onboard configuration circuitry
  - 2X16MB Quad SPI Flash
  - SDIO Card Interface (boot)
  - PC4 and 20 pin JTAG ports
- Memory
  - DDR3 Component Memory 1Gb (PS)
  - DDR3 SODIM Memory 1Gb (PL)
  - 2X16MB Quad SPI Flash (config)
  - IIC - 1 KB EEPROM
- Communication & Networking
  - PCIe Gen2x4
  - SFP+ and SMA Pairs
  - GigE RGMII Ethernet (PS)
  - USB OTG 1 (PS) – Host USB
  - IIC Bus Headers/HUB (PS)
  - 1 CAN with Wake on CAN (PS)
  - USB UART (PS)
- Video/Display
  - HDMI 8 color RGB 4.4.4 1080P-60 OUT
  - HDMI IN 8 color RGB 4.4.4
- Power
  - 12 V wall adapter
  - Current measurement capability of supplies
- Expansion Connectors
  - 1st FMC LPC expansion port (LA LVDS Bus. 1 GT)
  - 2nd FMC HPC expansion port (LA & HA LVDS Bus, 8 GT – No HB bus)
  - Dual Pmod (8 I/O Shared with LED's)
  - Single Pmod (4 I/O)
  - IIC access to 8 I/O
- Clocking
  - 33MHz PS System Clock
  - 200MHz PL Oscillator (Single-Ended Differential)
  - SMA Connectors for external clock (Differential)
  - GTX Reference Clock port with 2 SMA connectors
  - OBSAI/CPRI – SFP+ Received clock
  - EXT Config CLK
- Control & I/O
  - 2 User Push Buttons/Dip Switch, 2 User LEDs
  - IIC access to GPIO
  - SDIO (SD Card slot)
  - 3 User Push Buttons, 2 User Switches, 8 User LEDs
  - IIC access to 8 I/O
  - IIC access to a WTClock

To purchase this kit, visit www.em.avnet.com/z7jesd204bkit

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Block Diagram

The ZC706 evaluation board block diagram.



Note: Page numbers reference the page number of schematic 0381513.

Featured Manufacturers

XILINX
ALL PROGRAMMABLE™

Parts:

Part Number	Description	Resale
AES-Z7-JESD3-G	Xilinx Zynq-7000 All Programmable SoC ZC706 Evaluation Kit Optimized for JESD204B	$3275.00 USD

Related Parts:

Part Number	Description	Resale
AD-FMCDAQ2-EBZ	High-Speed Data Acquisition FMC Module with JESD204B	$1380.00 USD
AES-KCU-JESD-G	Xilinx Kintex® UltraScale™ FPGA DSP Development Kit with JESD204B High-Speed Analog	$5499.00 USD

Countries Available for Purchase: Americas, EMEA, Asia, Japan

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